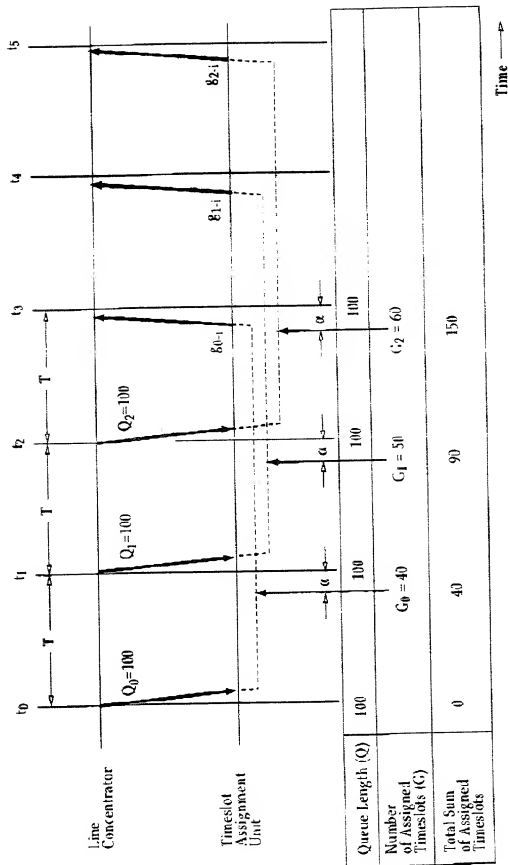


FIG. 1
Prior Art



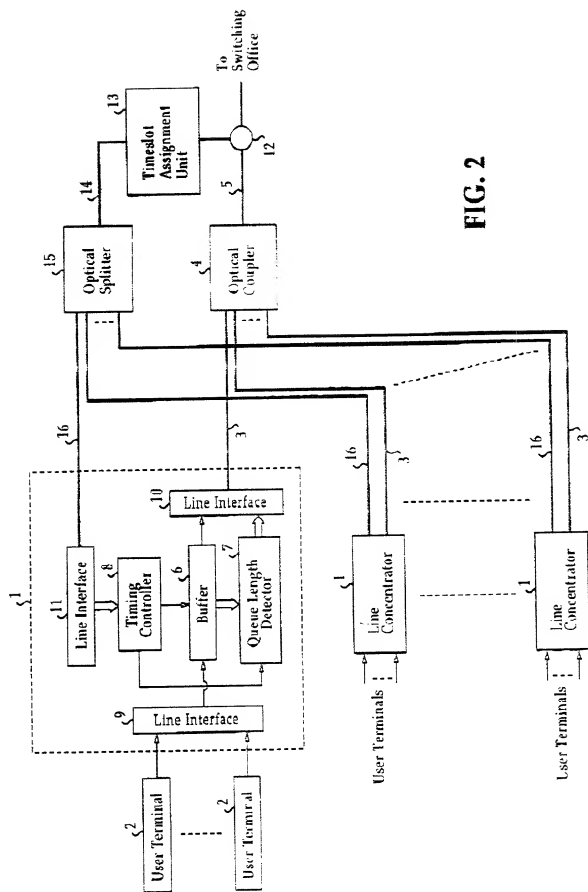


FIG. 3

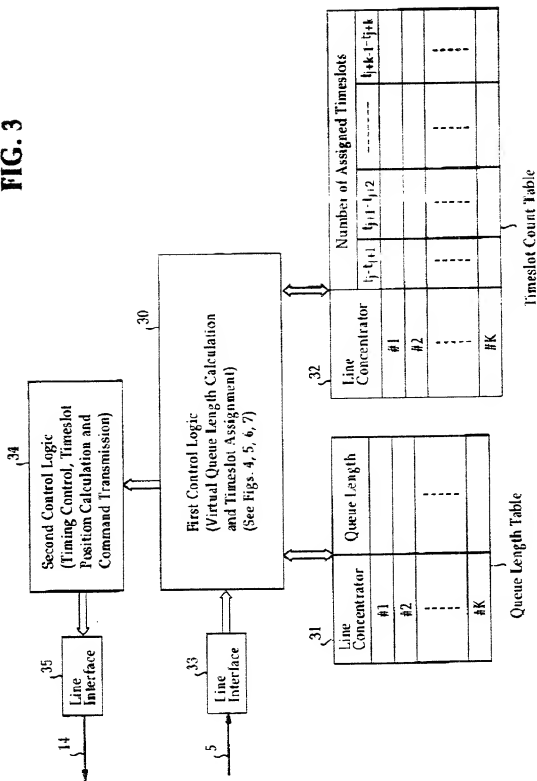


FIG. 4

First Control Logic 30

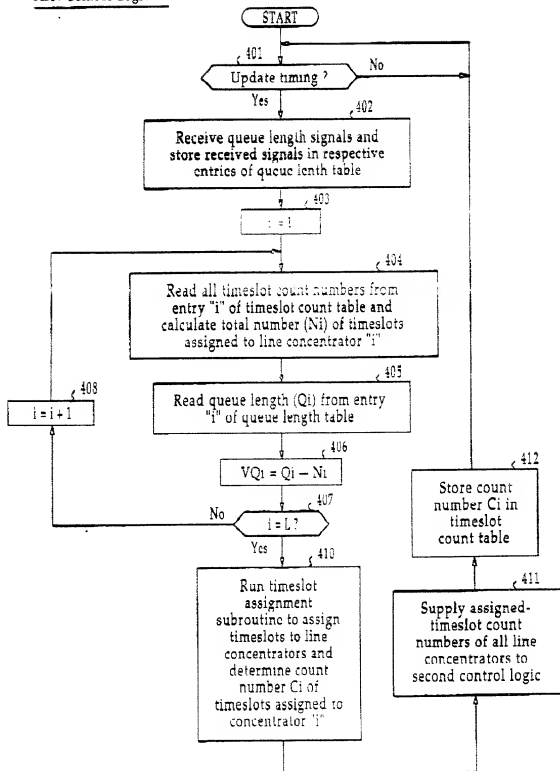


FIG. 5

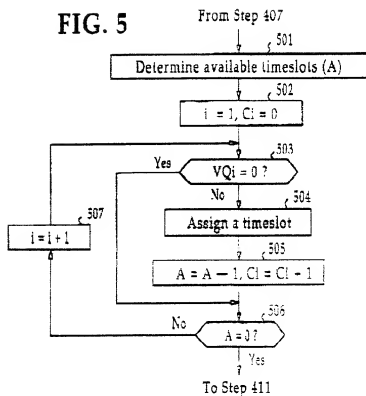


FIG. 6

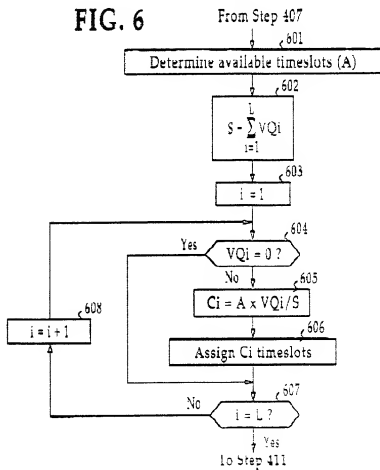


FIG. 7

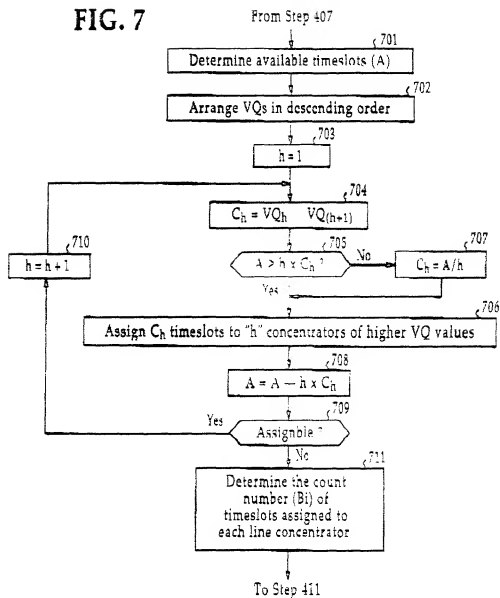


FIG. 8

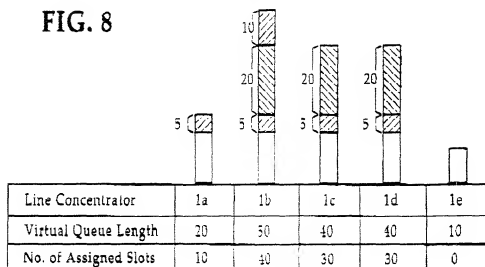
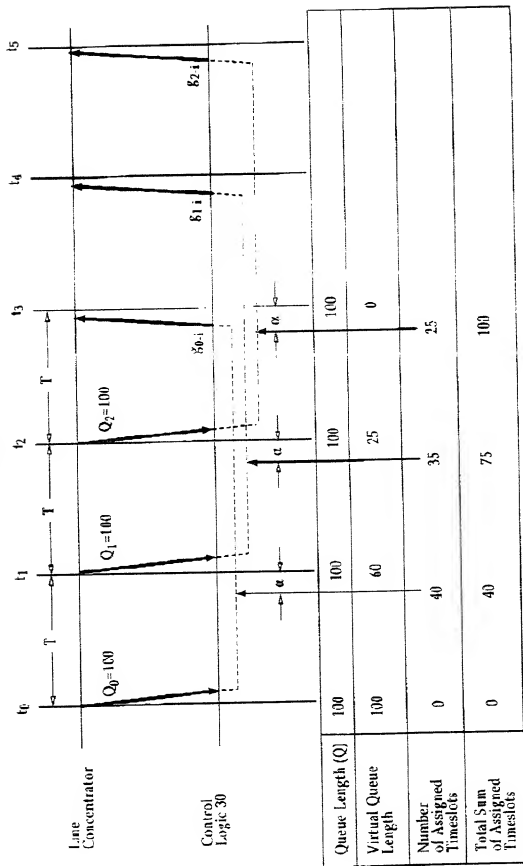


FIG. 9



Timeslot
Assignment
Unit 13A

FIG. 12

Line Concentrator

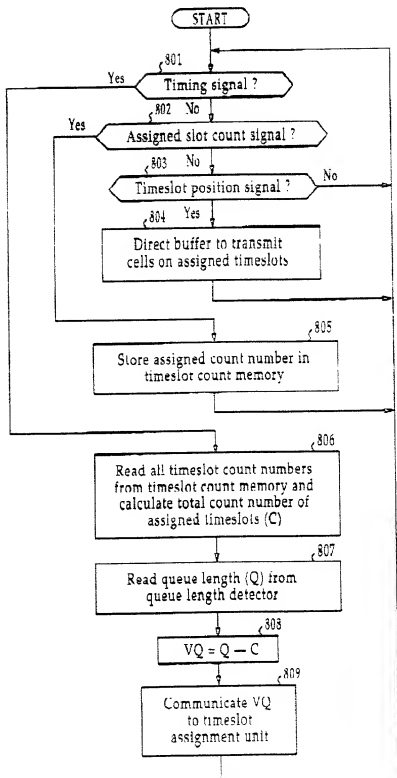


FIG. 13

First Control Logic 30A

